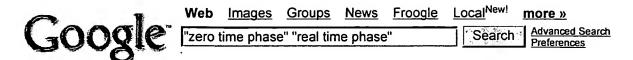
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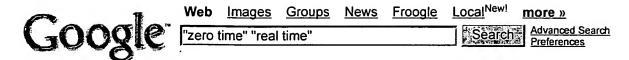
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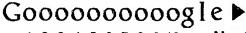
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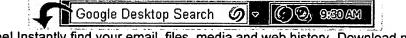
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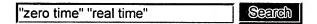
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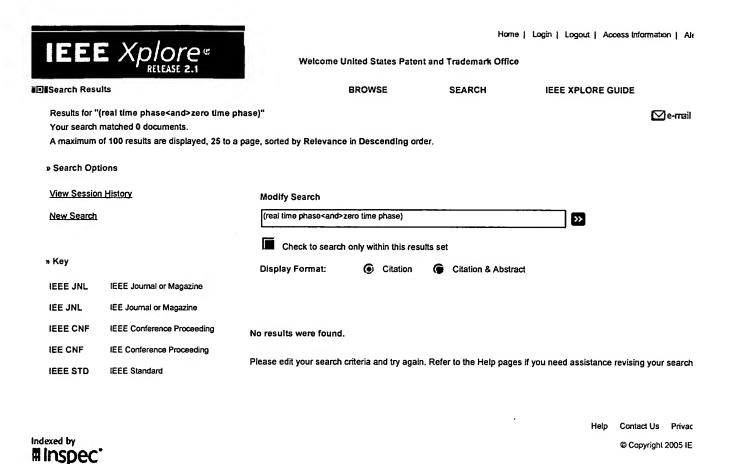
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2

1. Integrating model-based design and preemptive scheduling in mixed time- and event-trigger Scaife, N.; Caspi, P.;

Real-Time Systems, 2004. ECRTS 2004. Proceedings. 16th Euromicro Conference on

30 June-2 July 2004 Page(s):119 - 126

Digital Object Identifier 10.1109/EMRTS.2004.1311013

AbstractPlus | Full Text: PDF(258 KB) | IEEE CNF

2. Proving properties of real-time systems through logical specifications and Petri net models

Felder, M.; Mandrioli, D.; Morzenti, A.;

Software Engineering, IEEE Transactions on Volume 20, Issue 2, Feb. 1994 Page(s):127 - 141

Digital Object Identifier 10.1109/32.265634

AbstractPlus | Full Text: PDF(1376 KB) | IEEE JNL

3. Computationally efficient algorithms for multiple fault diagnosis in large graph-based system

Fang Tu; Pattipati, K.R.; Deb, S.; Malepati, V.N.;

Systems, Man and Cybernetics, Part A, IEEE Transactions on

Volume 33, Issue 1, Jan. 2003 Page(s):73 - 85

Digital Object Identifier 10.1109/TSMCA.2003.809222

AbstractPlus | References | Full Text: PDF(920 KB) | IEEE JNL

4. Automotive virtual integration platforms: why's, what's, and how's

Giusto, P.; Ferrari, A.; Lavagno, L.; Brunel, J.-Y.; Fourgeau, E.; Sangiovanni-Vincentelli, A.; Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 2002 IEEE International

16-18 Sept. 2002 Page(s):370 - 378

Digital Object Identifier 10.1109/ICCD.2002.1106796

AbstractPlus | Full Text: PDF(1260 KB) | IEEE CNF

5. IEEE standard for information technology-telecommunications and information exchange be and metropolitan area networks-specific requirements-part 17: resilient packet ring (RPR) ac physical layer specifications

IEEE Std 802.17-2004

2004 Page(s):0_1 - 664

AbstractPlus | Full Text: PDF(5472 KB) | IEEE STD

Direction finding on spread-spectrum signals using the time-domain filtered cross spectral (Houghton, A.W.; Reeve, C.D.;

Radar, Sonar and Navigation, IEE Proceedings -Volume 144, Issue 6, Dec. 1997 Page(s):315 - 320 AbstractPlus | Full Text: PDF(568 KB) | IEE JNL

7. A low complexity packet detection algorithm for a CPM modem

Penrod, R.; Fitz, M.P.; Weijun Zhu; Takeshita, O.;

Signals, Systems and Computers, 2004. Conference Record of the Thirty-Eighth Asilomar Conference

Volume 1, 7-10 Nov. 2004 Page(s):1062 - 1067 Vol.1 Digital Object Identifier 10.1109/ACSSC.2004.1399303

AbstractPlus | Full Text: PDF(704 KB) | IEEE CNF

8. A synchronous real-time knowledge-based system

Kaestner, C.A.A.; Farines, J.-M.;

2

1,2

Real-Time Systems, 1995. Proceedings., Seventh Euromicro Workshop on

14-16 June 1995 Page(s):205 - 212

Digital Object Identifier 10.1109/EMWRTS.1995.514313

AbstractPlus | Full Text: PDF(708 KB) | IEEE CNF

9. IEEE Standard for Modeling and Simulation [M and S] High Level Architecture [HLA] - Feder:

IEEE Std 1516.1-2000

2001 Page(s):i - 467

AbstractPlus | Full Text: PDF(2276 KB) IEEE STD

10. State-based model checking of event-driven system requirements

Atlee, J.M.; Gannon, J.;

Software Engineering, IEEE Transactions on

Volume 19, Issue 1, Jan. 1993 Page(s):24 - 40

Digital Object Identifier 10.1109/32.210305

AbstractPlus | Full Text: PDF(1340 KB) IEEE JNL

11. A timeout-based message ordering protocol for a lightweight software implementation of Th 1

Ezhilchelvan, P.D.; Brasileiro, F.V.; Speirs, N.A.;

Parallel and Distributed Systems, IEEE Transactions on

Volume 15, Issue 1, Jan. 2004 Page(s):53 - 65

Digital Object Identifier 10.1109/TPDS.2004.1264786

AbstractPlus | References | Full Text: PDF(448 KB) | IEEE JNL

12. Method of time Petrl net analysis for analysis of fault trees with time dependencies

Magott, J.: Skrobanek, P.:

Computers and Digital Techniques, IEE Proceedings-

Volume 149, Issue 6, Nov. 2002 Page(s):257 - 271

Digital Object Identifier 10.1049/ip-cdt:20020804

AbstractPlus | Full Text: PDF(820 KB) | IEE JNL

200 13. Pfair scheduling of periodic tasks with allocation constraints on multiple processors

Liu, D.; Lee, Y.-H.;

Parallel and Distributed Processing Symposium, 2004. Proceedings. 18th International

26-30 April 2004 Page(s):119

Digital Object Identifier 10.1109/IPDPS.2004.1303076

AbstractPlus | Full Text: PDF(1382 KB) IEEE CNF

14. Worst-case execution times and schedulability analysis of statecharts models 22

Erpenbach, E.; Altenbernd, P.;

Real-Time Systems, 1999. Proceedings of the 11th Euromicro Conference on

9-11 June 1999 Page(s):70 - 77

Digital Object Identifier 10.1109/EMRTS.1999.777452

AbstractPlus | Full Text: PDF(484 KB) | IEEE CNF

15. Modelling And Performance Evaluation Of Flexible Manufacturing Systems Using Determinitioned Petri Nets

Elehera, T.K.; Mishra, B.S.; Patnaik, L.M.; Girault, C.;

Factory 2000 - Advanced Factory Automation, Fourth International Conference on (Conf. Publ. No. 3-5 Oct 1994 Page(s):362 - 368

AbstractPlus | Full Text: PDF(532 KB) | IEE CNF

16. Correctness Verification and Performance Analysis of Real-Time Systems Using Stochastic
Petri Nets

Bucci, G.; Sassoli, L.; Vicario, E.;

Software Engineering, IEEE Transactions on

Volume 31, Issue 11, Nov. 2005 Page(s):913 - 927

Digital Object Identifier 10.1109/TSE.2005.122

AbstractPlus | Full Text: PDF(784 KB) | IEEE JNL

17. Communication in time-frequency spread media using adaptive equalization

Di Toro, M.J.:

44

Proceedings of the IEEE

Volume 56, Issue 10, Oct. 1968 Page(s):1653 - 1679

AbstractPlus | Full Text: PDF(4671 KB) | IEEE JNL

18. The SIFT algorithm for fundamental frequency estimation

Markel, J.:

Audio and Electroacoustics, IEEE Transactions on

Volume 20, Issue 5, Dec 1972 Page(s):367 - 377

AbstractPlus | Full Text: PDF(1176 KB) | IEEE JNL

19. A hardware processor for implementing the pyramid vector quantizer

Qureshi, Q.A.; Fischer, T.;

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEE

Volume 37, Issue 7, July 1989 Page(s):1135 - 1142

Digital Object Identifier 10.1109/29.32288

AbstractPlus | Full Text: PDF(700 KB) | IEEE JNL

20. Reasoning about time in higher-level language software

Shaw, A.C.;

Software Engineering, IEEE Transactions on

Volume 15, Issue 7, July 1989 Page(s):875 - 889

Digital Object Identifier 10.1109/32.29487

AbstractPlus | Full Text: PDF(1160 KB) | IEEE JNL

21. Coverage modeling for dependability analysis of fault-tolerant systems

Dugan, J.B.; Trivedi, K.S.;

Computers, IEEE Transactions on

Volume 38, Issue 6, June 1989 Page(s):775 - 787

Digital Object Identifier 10.1109/12.24286

AbstractPlus | Full Text: PDF(1172 KB) IEEE JNL

22. A flux observer for induction machines based on a time-variant discrete model

Bottura, C.P.; Silvino, J.L.; de Resende, P.;

Industry Applications, IEEE Transactions on

Volume 29, Issue 2, March-April 1993 Page(s):349 - 354

Digital Object Identifier 10.1109/28.216543

AbstractPlus | Full Text: PDF(464 KB) | IEEE JNL

23. Fault detection/monitoring using time Petri nets

Srinivasan, V.S.; Jafari, M.A.;

Systems, Man and Cybernetics, IEEE Transactions on Volume 23, Issue 4, July-Aug. 1993 Page(s):1155 - 1162

Digital Object Identifier 10.1109/21.247896

AbstractPlus | Full Text: PDF(724 KB) | IEEE JNL

24. Vertical spatial coherence model for a translent signal forward-scattered from the sea surface

Yoerger, E.J.; McDaniel, S.T.;

Oceanic Engineering, IEEE Journal of

Volume 21, Issue 1, Jan. 1996 Page(s):24 - 36

Digital Object Identifier 10.1109/48.485199

AbstractPlus | References | Full Text: PDF(1144 KB) | IEEE JNL

25. A programming model and system infrastructure for real-time synchronization in distributed

Blair, G.S.; Coulson, G.; Papathomas, M.; Robin, P.; Stefani, J.-B.; Horn, F.; Hazard, L.;

Selected Areas in Communications, IEEE Journal on

Volume 14, Issue 1, Jan. 1996 Page(s):249 - 263

Digital Object Identifier 10.1109/49.481709

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Specification

IEEE Std 1516.1-2000 2001 Page(s):i - 467

AbstractPlus | Full Text: PDF(2276 KB) | IEEE STD

2. Distributed simulation of discrete event systems

Righter, R.; Walrand, J.C.;

Proceedings of the IEEE

Volume 77, Issue 1, Jan. 1989 Page(s):99 - 113

Digital Object Identifier 10.1109/5.21073

AbstractPlus | Full Text: PDF(1520 KB) | IEEE JNL

3. A window protocol for transmission of time-constrained messages

Zhao, W.; Stankovic, J.A.; Ramamritham, K.;

Computers, IEEE Transactions on

Volume 39, Issue 9, Sept. 1990 Page(s):1186 - 1203

Digital Object Identifier 10.1109/12.57059

AbstractPlus | Full Text: PDF(1324 KB) | IEEE JNL

4. Design issues in parallel simulation languages

Rajaei, H.; Ayani, R.;

Design & Test of Computers, IEEE

Volume 10, Issue 4, Dec. 1993 Page(s):52 - 63

Digital Object Identifier 10.1109/54.245963

AbstractPlus | Full Text: PDF(1212 KB) | IEEE JNL

5. Petri nets and Industrial applications: A tutorial

Zurawski, R.; MengChu Zhou;

Industrial Electronics, IEEE Transactions on

Volume 41, Issue 6, Dec. 1994 Page(s):567 - 583 Digital Object Identifier 10.1109/41.334574

AbstractPlus | Full Text: PDF(1548 KB) | IEEE JNL

An office analysis methodology using Petri nets and playscripts Ang, J.S.K.; Conrath, D.W.;

Systems, Man and Cybernetics, Part A, IEEE Transactions on

Volume 26, Issue 5, Sept. 1996 Page(s):572 - 582 Digital Object Identifier 10.1109/3468.531905

AbstractPlus | References | Full Text: PDF(1044 KB) | IEEE JNL

7. Analysis of end-to-end QoS for networked virtual reality services in UMTS 44

Skorin-Kapov, L.; Huljenic, D.; Mikic, D.; Vilendecic, D.;

Communications Magazine, IEEE

Volume 42, Issue 4, Apr 2004 Page(s):49 - 55

Digital Object Identifier 10.1109/MCOM.2004.1284929

AbstractPlus | Full Text: PDF(233 KB) | IEEE JNL

8. Evaluation of secure peer-to-peer overlay routing for survivable SCADA systems \$ 7

Farris, J.J.; Nicol, D.M.;

Simulation Conference, 2004. Proceedings of the 2004 Winter

Volume 1, 5-8 Dec. 2004 Page(s):

Digital Object Identifier 10.1109/WSC.2004.1371330

AbstractPlus | Full Text: PDF(358 KB) | IEEE CNF

9. Software and Simulation Modeling for Real-Time Software-Intensive Systems 12

Dongping Huang; Sarjoughian, H.;

Distributed Simulation and Real-Time Applications, 2004. DS-RT 2004. Eighth IEEE International S

21-23 Oct. 2004 Page(s):196 - 203

Digital Object Identifier 10.1109/DS-RT.2004.37

AbstractPlus | Full Text: PDF(144 KB) | IEEE CNF

10. Approximate real-time clocks for scheduled events

Fetzer, C.; Raynal, M.;

Object-Oriented Real-Time Distributed Computing, 2002. (ISORC 2002). Proceedings. Fifth IEEE II

Symposium on

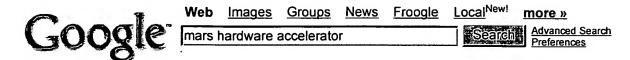
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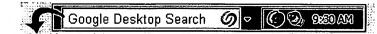
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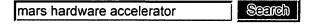
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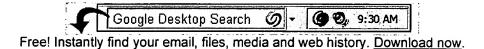
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